

SSCN143XGS6

NPN Type Digital Transistor (built-in resistors)

Features

vcc	VIN	Ю	R1	R2/R1 Typ.
50V	-7~+20V	100mA	4.7kΩ	2.1

> Description

Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).

The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects. Only the on/off conditions need to be set for operation, making the device design easy.

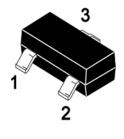
Applications

- Amplifying signal
- Electronic switch
- Oscillating circuit
- Variable resistance

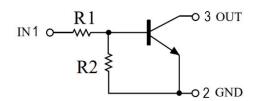
Ordering Information

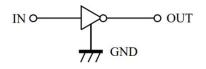
Device	Package	Shipping
SSCN143XGS6	SOT-23	3000/Reel

Pin configuration

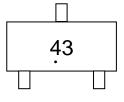


SOT-23





Circuit Diagram



Marking (Top View)





\succ Absolute Maximum Ratings (T_A=25 $^{\circ}$ C unless otherwise noted)

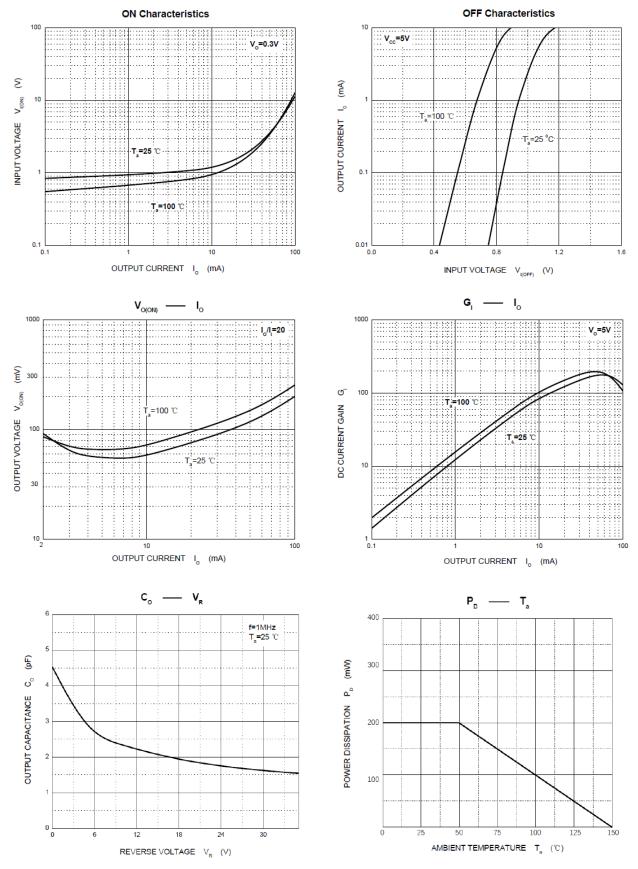
Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	50	V
Input Voltage	V _{CN}	-7 to +20	V
Output current	lo	100	mA
Power Dissipation	P _D	200	mW
Junction Temperature	TJ	-55 to 150	$^{\circ}$
Storage Temperature	T _{STG}	-55 to 150	$^{\circ}$

\succ Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Input Voltage	$V_{I(off)}$	Vcc = 5V, Io=0.1mA	0.3			V
Input Voltage	V _{I(on)}	$V_{CC} = 0.3V, I_{O} = 20mA$			2.5	V
Output Voltage	$V_{O(on)}$	$I_0/I_1 = 10 \text{mA}/0.5 \text{mA}$		0.1	0.3	V
Input Current	lı	V _I = 5V			1.8	mA
Output Current	I _{O(off)}	Vcc = 50V, Vı = 0V			0.5	uA
DC Current Gain	G ₁	V _O = 5V, I _O = 10mA	30			
Input Resistance	R ₁		3.29	4.7	6.11	ΚΩ
Resistance Ration	R ₂ /R ₁		1.7	2.1	2.6	
Transition Frequency	f⊤	Vo=10V, Io=5mA, f=100MHz		250		MHz



> Typical Performance Characteristics (T_A=25℃ unless otherwise noted)

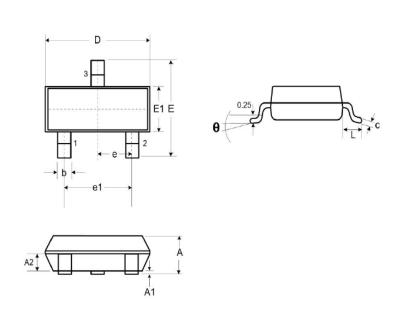




Package Information

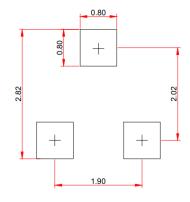
Mechanical Data

SOT-23



DIM	Millimeters				
DIM	Min.	Тур.	Max.		
Α	0.89	-	1.12		
A 1	0.01	-	0.10		
A2	0.88	0.95	1.02		
b	0.30	-	0.51		
С	0.08	-	0.18		
D	2.80	2.90	3.04		
E	2.10	2.37	2.64		
E1	1.20	1.30	1.40		
е	0.95				
e1	1.90				
L	0.40	0.50	0.60		
L1	0.55				
N	3				
θ	0°	-	8°		

Recommended Pad outline (Unit: mm)





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